

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Yong Wang et al.

Confirmation No.:

Application No.:

Examiner:

Filing Date: 12/30/03

Group Art Unit:

Title: System and Method for Integrating Subcircuit Models in an Integrated Power Grid Analysis Environment

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or  
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:  
☐ Statement under 37 CFR 1.97(e), or  
☐ a \$180.00 fee under 37 CFR 1.17(p), or  
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:  
☐ Statement under 37 CFR 1.97(e)(1) or (2), and  
☐ a \$180.00 fee set forth in 37 CFR 1.17(p).  
(Filed after final action, a notice of allowance, on or before payment of the issue fee)

Please charge to Deposit Account **08-2025** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **08-2025** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Statement together with any required copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.


It is requested that the information disclosed herein be made of record in this application.

"Express Mail" label no. **EV386626472US**

Date of Deposit **12/30/03**

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

By



Typed Name: **Shreen K. Danamraj**

Respectfully submitted,

**Yong Wang et al.**

By



**Shreen K. Danamraj**

Attorney/Agent for Applicant(s)

Reg. No. **41,696**

Date: **12/30/03**

<b>FORM PTO-1449</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	<b>200312082-1</b>		
	APPLICANT		
	<b>Yong Wang et al.</b>		
	FILING DATE	GROUP	
	<b>12/30/03</b>		

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A	6,151,568	11-21-2000	Allen et al.	
	1B	2002/0112212	08-15-2002	Cohn et al.	
	1C	6,440,770	08-27-2002	Banerjee et al.	
	1D	2003/0001284	01-02-2003	List et al.	
	1E	6,534,723	03-18-2003	Asai et al.	
	1F	6,555,921	04-29-2003	Kwon et al.	
	1G	2003/0088395	05-08-2003	Frech et al.	
	1H				
	1I				
	1J				
	1K				

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	Joseph N. Kozhaya et al.; "A Multigrid-like Technique for Power Grid Analysis"; Proceedings of the 2001 IEEE/ACM International Conference on CAD, San Jose, CA, November 4-8, 2001; pages 1-13.
1R	Srinivas Bodapati et al.; "High-Level Current Macro-Model For Power-Grid Analysis"; Annual ACM IEEE Design Automation Conference; New Orleans, LA, June 10-14, 2002; 6 pages.
1S	Yu-Min Lee et al.; "Power Grid Transient Simulation in Linear Time Based on Transmission-Line-Modeling Alternating-Direction-Implicit Method"; IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 21, No. 11, November 2002; pages 1343-1352.

EXAMINER

DATE CONSIDERED

FORM PTO-1449

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	1L					
	1M					
	1N					
	1O					
	1P					

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	Yong Wang et al.; "Integrated Power Grid Modeling and Analysis"; 12th Topical Meeting on Electrical Performance of Electronic Packaging, Princeton, NJ, October 27-29, 2003; pages 1-4.
1R	
1S	

EXAMINER

DATE CONSIDERED